

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Takashi KOBAYASHI et al.

Appln. No.:

Filed: HERewith

For: NONVOLATILE SEMICONDUCTOR MEMORY DEVICE AND
METHOD OF MANUFACTURING THEREOF

* * *

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. § 1.56, and without any
assertion as to materiality or prior art effect, the
documents listed on the attached Form PTO-1449 are hereby
cited.

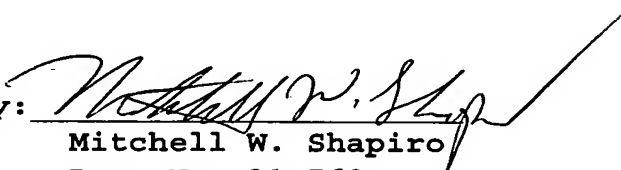
The documents on the attached List were cited in the
specification, on pages 1, 3 and 23, and their relevance is
indicated therein.

Respectfully submitted,

MWS:sjk

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By:


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November 26, 2003

FORM PTO-1449				Atty. Docket No. XA-10000		Appln. No.	
<u>LIST OF DOCUMENTS CITED BY APPLICANT</u>				Applicant Takashi KOBAYASHI et al.			
				Filing Date HEREWITH		Group	
U.S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Sub-class	Filing Date
	AA	6,011,725	01/04/00	Eitan	365	185.33	
	AB	5,966,603	10/12/99	Eitan	438	258	
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
FOREIGN PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Country	Class	Sub-class	Translation
	AJ	5-75133	03/26/93	Japan			Abstract
	AK						
	AL						
	AM						
	AN						
	AO						
OTHER (including author, title, date, pertinent pages, etc.)							
	AP						
	AQ						
	AR						
Examiner				Date Considered			
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.							